



#5A/11-2203
v. Sony

PATENT

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE
ART UNIT 2133

Examiner Shelly A. Chase

RECEIVED

NOV 18 2003

Technology Center 2100

IN THE PATENT APPLICATION OF John H. Cook III, et al.

CASE: 10003744-1

SERIAL NO: 09/672,650

FILED: 28 September 2000

SUBJECT: MEMORY TESTER HAS MEMORY SETS CONFIGURABLE FOR USE AS
ERROR CATCH RAM, TAG RAM'S, BUFFER MEMORIES AND STIMULUS
LOG RAM

COMMISSIONER FOR PATENTS

P.O. BOX 1450

ALEXANDRIA, VIRGINIA, 22313-1450

SIR:

AMENDMENT "A"

In timely response to an Office Action mailed in the above-captioned Patent Application on 7 July 2003, which set a three month shortened statutory period for response expiring 7 October 2003, and for which the original due date was once extended to 7 November 2003 in accordance with 37 CFR 1.136(a), please amend the Application as hereinafter requested pursuant to MPEP § 714 III (1-4):

11/13/2003 HVDUNG1 00000089 501078 09672650

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